



# APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Vimal Jyothi Engineering College S7 B.Tech Exam 2021

24 'th February -2021 AN Time 1.30-4 .30 PM

## SEATING ARRANGEMENT



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### S8CE A class room - GF

		FROM	TO	TOTAL		
KNOWLEDGE ENGINEERING (IT407)		PKD17IT025	PKD17IT025	1		
KNOWLEDGE ENGINEERING (IT407)		VJC17IT020	VJC17IT020	1		
DISTRIBUTED COMPUTING (CS407)		CHN17CS004	CHN17CS106	2		
		JBT16CS012	JBT16CS012	1		
		MAC17CS023	MAC17CS023	1		
		RCE17CS004	RCE17CS004	1		
		RET17CS009	RET17CS009	1		
		TLY17CS036	TLY17CS036	1		
		VML16CS007	VML16CS045	3		
		VML17CS001	VML17CS005	3		
TRANSPORTATION ENGINEERING II (CE407)		VML17CE057	VML17CE085	15		
			Total	30		
	A	B	C	D	E	F
	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>
<b>1</b>	PKD17IT025	VML17CE057	MAC17CS023	VML17CE065	VML16CS019	VML17CE078
<b>2</b>	VJC17IT020	VML17CE058	RCE17CS004	VML17CE067	VML16CS045	VML17CE080
<b>3</b>	CHN17CS004	VML17CE059	RET17CS009	VML17CE069	VML17CS001	VML17CE081
<b>4</b>	CHN17CS106	VML17CE061	TLY17CS036	VML17CE071	VML17CS004	VML17CE082
<b>5</b>	JBT16CS012	VML17CE064	VML16CS007	VML17CE074	VML17CS005	VML17CE085

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S8 CE B class room -GF



	<b>subject</b>	<b>FROM</b>	<b>TO</b>	<b>TOTAL</b>
	DISTRIBUTED COMPUTING (CS407)	VML17CS008	VML17CS024	15
	TRANSPORTATION ENGINEERING II (CE407)	VML17CE088	VML17CE118	15
			Total	30

	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>	<b>E</b>	<b>F</b>
	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>
<b>1</b>	VML17CE088	VML17CS008	VML17CE099	VML17CS013	VML17CE105	VML17CS018
<b>2</b>	VML17CE093	VML17CS009	VML17CE101	VML17CS014	VML17CE108	VML17CS020
<b>3</b>	VML17CE095	VML17CS010	VML17CE102	VML17CS015	VML17CE110	VML17CS021
<b>4</b>	VML17CE097	VML17CS011	VML17CE103	VML17CS016	VML17CE111	VML17CS022
<b>5</b>	VML17CE098	VML17CS012	VML17CE104	VML17CS017	VML17CE118	VML17CS024

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**S4 CE A class room -GF**

	<b>subject</b>	<b>FROM</b>	<b>TO</b>	<b>TOTAL</b>
	DISTRIBUTED COMPUTING (CS407)	VML17CS025	VML17CS050	15
	TRANSPORTATION ENGINEERING II (CE407)	VML16CE102	VML16CE102	1
		VML17CE001	VML17CE026	14
			Total	30

	A	B	C	D	E	F
	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>
<b>1</b>	VML17CS025	VML16CE102	VML17CS032	VML17CE008	VML17CS040	VML17CE017
<b>2</b>	VML17CS027	VML17CE001	VML17CS033	VML17CE011	VML17CS046	VML17CE020
<b>3</b>	VML17CS028	VML17CE003	VML17CS034	VML17CE012	VML17CS047	VML17CE021
<b>4</b>	VML17CS030	VML17CE005	VML17CS036	VML17CE013	VML17CS049	VML17CE024
<b>5</b>	VML17CS031	VML17CE007	VML17CS039	VML17CE016	VML17CS050	VML17CE026

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**S4 CE B Class room -GF**



subject	FROM	TO	TOTAL
DISTRIBUTED COMPUTING (CS407)	VML17CS052	VML17CS057	4
DIGITAL CONTROL SYSTEM (AE407)	LCHN17AE029	LCHN17AE029	1
DIGITAL CONTROL SYSTEM (AE407)	LVML17AE023	LVML17AE024	2
DIGITAL CONTROL SYSTEM (AE407)	VML16AE004	VML16AE030	3
DIGITAL CONTROL SYSTEM (AE407)	VML17AE001	VML17AE008	5
TRANSPORTATION ENGINEERING II (CE407)	VML17CE027	VML17CE056	15
		Total	30

	A	B	C	D	E	F
	Reg no	Reg no	Reg no	Reg no	Reg no	Reg no
<b>1</b>	VML17CE027	VML17CS052	VML17CE034	LVML17AE023	VML17CE049	VML17AE001
<b>2</b>	VML17CE028	VML17CS054	VML17CE038	LVML17AE024	VML17CE053	VML17AE003
<b>3</b>	VML17CE031	VML17CS055	VML17CE039	VML16AE004	VML17CE054	VML17AE004
<b>4</b>	VML17CE032	VML17CS057	VML17CE042	VML16AE026	VML17CE055	VML17AE006
<b>5</b>	VML17CE033	LCHN17AE029	VML17CE044	VML16AE030	VML17CE056	VML17AE008



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**S1 CE A class room-GF**

	<b>subject</b>	<b>FROM</b>	<b>TO</b>	<b>TOTAL</b>
TRANSPORTATION ENGINEERING II (CE407)		JEC16CE061	JEC16CE061	1
		MAC17CE065	MAC17CE086	2
		UNT17CE014	UNT17CE014	1
	DIGITAL CONTROL SYSTEM (AE407)	VML17AE010	VML17AE022	11
			Total	15

	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>	<b>E</b>	<b>F</b>
	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>	<b>Reg no</b>
<b>1</b>	JEC16CE061	0	VML17AE011	0	VML17AE018	0
<b>2</b>	MAC17CE065	0	VML17AE014	0	VML17AE019	0
<b>3</b>	MAC17CE086	0	VML17AE015	0	VML17AE020	0
<b>4</b>	UNT17CE014	0	VML17AE016	0	VML17AE021	0
<b>5</b>	VML17AE010	0	VML17AE017	0	VML17AE022	0